

REMARKS

Favorable reconsideration of this application as presently amended and in light of the following discussion is respectfully requested.

Claims 1, 3-8, and 16-17 are pending in the present application. In the present Amendment, Claim 16 is amended without introducing any new matter to correct minor formal issues. These changes also do not raise any questions of new issues that would require further search and/or consideration.

In the October 27, 2008 Office Action, Claim 16 was rejected under 35 U.S.C. § 112, second paragraph, as being indefinite. Claim 17 was rejected under 35 U.S.C. § 112, first paragraph, as failing to comply with the written description requirement. Claims 1, and 3-8 were allowed.

Applicants wish to thank the Examiner for the indication of allowable subject matter in the Office Action.

In response to the rejection of Claim 16 for formal issues under 35 U.S.C. § 112, Claim 16 is amended to recite that the “third logic means [is] processing said original foreground image signals ... to generate a first superposed image,” and to recite “superposing said peripheral edge regions of said low-pass filtered foreground image ... to generate a second superposed image.” In light of the recital of a first and second superposed image, the claim is further formally amended to delete the portions directed to “result of said superposing” with the respective first and second superposed image. No new matter has been added. In addition, these changes are only formal in nature, so that these changes do not raise any questions of new issues that would require further search and/or consideration.

In response to the rejection of Claim 17 under 35 U.S.C. § 112, first paragraph, Applicants respectfully traverse the rejection. The specification clearly conveys to one of

ordinary skill in the art how to make and/or use the invention as recited in independent Claim 17, and therefore is sufficient to satisfy the enablement requirement..

First, the specification describes a hardware that can be used to perform the video processing method of Claim 1, and therefore also supports the method that can be executed by a computer. An exemplary computer hardware is schematically depicted in Figure 1. The specification provides:

Figure 1 schematically illustrates the overall system architecture of the PlayStation2. The system comprises: an Emotion Engine 100; a Graphics Synthesizer 200; a sound processor unit 300 having dynamic random access memory (DRAM); a read only memory (ROM) 400; a compact disc (CD) and digital versatile disc (DVD) unit 450; a Rambus Dynamic Random Access Memory (RDRAM) unit 500; an input/output processor 700 with dedicated RAM 750; and an external hard disk drive (HDD) 800.

(See Specification, p. 4, ll. 8-13.) Moreover, the specification provides a much more detailed discussion of this exemplary hardware from p. 4, l. 14, to p. 14, l. 13, including Figs. 1-8. In this description, many details on Graphics Synthesizer 200 are given. (See i.e. Fig. 4) In addition, the specification provides for a detailed explanation of the architecture of the Emotion Engine 100. Regarding Engine 100, the relevant portions of the specification recite:

Figure 2 schematically illustrates the architecture of the Emotion Engine 100 of Figure 1. The Emotion Engine 100 comprises: a floating point unit (FPU) 104; a central processing unit (CPU) core 102; vector unit zero (VU0) 106; vector unit one (VU1) 108; a graphics interface unit (GIF) 110; an interrupt controller (INTC) 112; a timer unit 114; a direct memory access controller 116; an image data processor unit (IPU) 116; a dynamic random access memory controller (DRAMC) 120; a sub-bus interface (SIF) 122; and all of these components are connected via a 128-bit main bus 124.

(See Specification, p. 5, l. 34, to p. 6, l. 5.) Starting on page 14, l. 14, Applicants' specification explains next how the video processing method is performed on the computer.

First, the step of "generating original foreground image signals" and the step of "preparing said background image for display" are described, where the CPU core 102 and the vector unit 108 of the Emotion Engine 100 can be used. (Specification, from p. 14, l. 14,

to p. 15, l. 15.). Next, the step of “applying anti-aliasing filtering to edges” is explained, and that it can be performed on the Graphics Synthesizer 200. (Specification, p. 15, ll. 16-20.) The remaining steps “first processing said primitive-processed image signals,” “second processing said original foreground image signals,” and “outputting said display signal for displaying said anti-aliased foreground image “ are also described in the specification, and that they can be performed on the Graphics Synthesizer 200. (Specification, p. 15, l. 32, to p. 17, l. 15.)

Moreover, the specification makes clear that these method steps can be implemented as a computer software, and because the method steps have been explained in clearly sufficient detail for one of ordinary skill in the art how to make and/or use them with reference to different hardware processing units, for example the Graphic Synthesizer, it is also clear that one of ordinary skill in the art would know how to implement such a software that could be stored onto a computer readable storage medium. (Specification, p. 17, ll. 16-18.) This contention is further supported by Claims 12-15 as originally filed.

Therefore, Applicants respectfully submit that the features of Claim 17 are clearly supported by the specification as originally filed, and that a person of ordinary skill in the art is clearly enabled how to make and/or use the invention.

The present amendment is submitted in accordance with the provisions of 37 C.F.R. § 1.116, which after Final Rejection permits entry of amendments placing the claims in better form for consideration on appeal. As the present amendment is believed to overcome outstanding rejections under 35 U.S.C. § 112, first and second paragraph, the present amendment places the application in better form for consideration on appeal. In addition, the changes to Claim 16 are only of formal nature, to address the rejection under 35 U.S.C. § 112, second paragraph. It is therefore respectfully requested that 37 C.F.R. § 1.116 be liberally construed, and that the present amendment be entered.

Consequently, in light of the present amendment, the present application is believed to be in condition for allowance and an early and favorable action to that effect is respectfully requested.

Should the Examiner deem that any further action is necessary to place this application in even better form for allowance, the Examiner is encouraged to contact Applicants' undersigned representative at the below listed telephone number.

Respectfully submitted,

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